ATTY DKT. No.: U.S. SERIAL No.: NVDA/P000876 UNKNOWN HEREWITH FILED: APPLICANT: TITLE: **NVIDIA CORPORATION** 

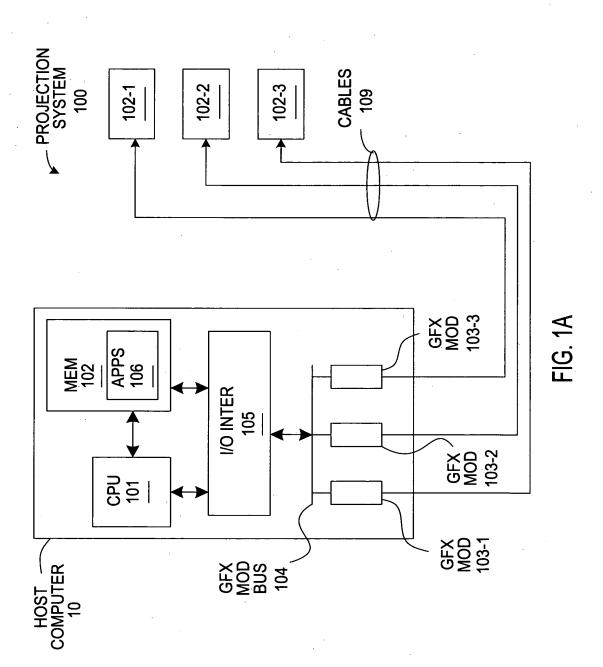
APPLICANT: INVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION

STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 1 OF 27

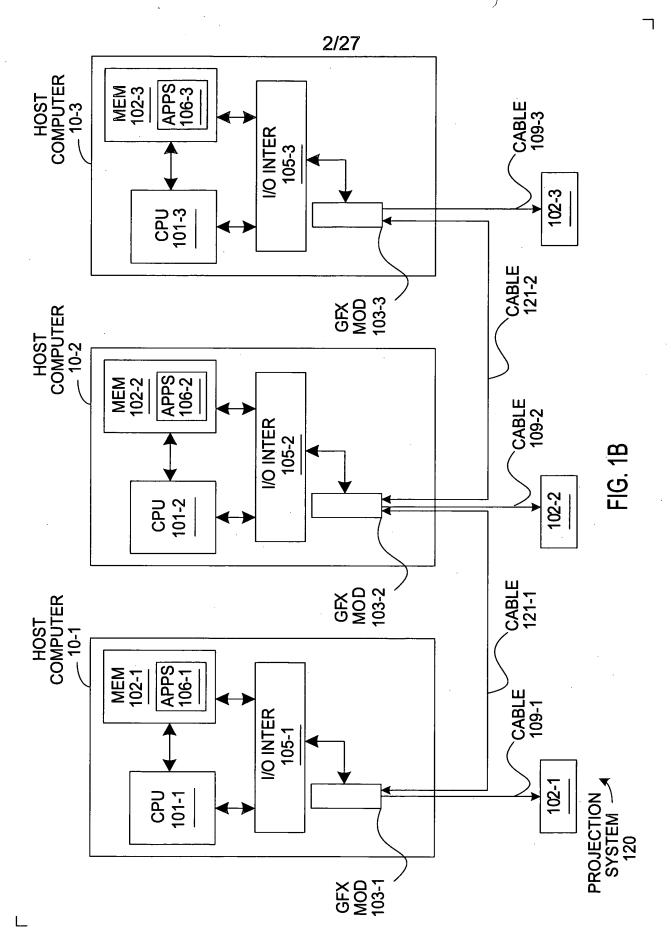


ATTY DKT. No.: U.S. SERIAL No.: FILED: APPLICANT:

NVDA/P000876
UNKNOWN
HEREWITH
NVIDIA CORPORATION
METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE TITLE:

INVENTOR: IAN M. WILLIAMS, ET AL. EXPRESS MAIL NO.: EV335471017US

PAGE 2 OF 27



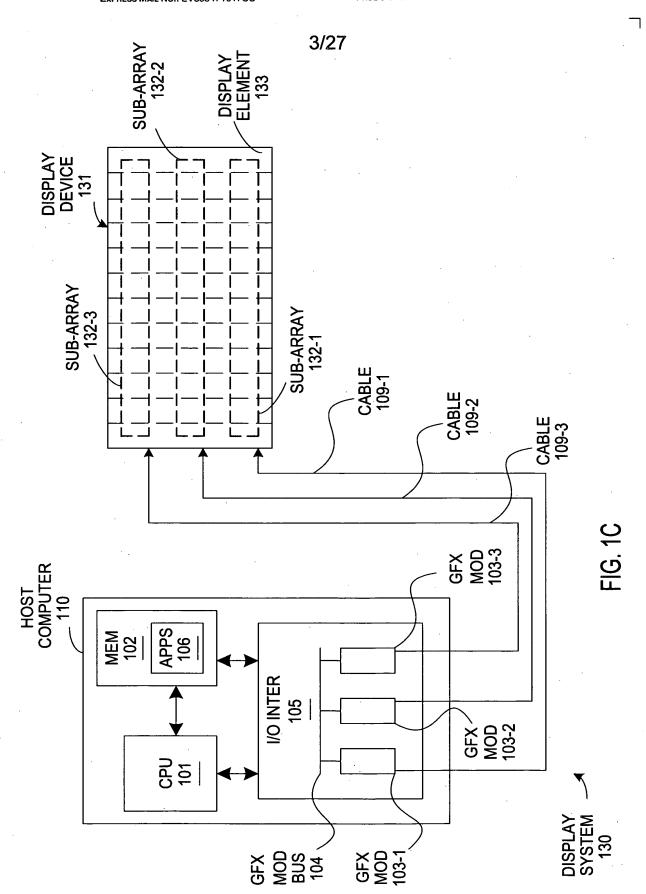
ATTY DKT. NO.: U.S. SERIAL NO.: FILED: APPLICANT: TITLE:

NVDA/P000876 UNKNOWN HEREWITH NVIDIA CORPORATION

METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL. EXPRESS MAIL NO.: EV335471017US

PAGE 3 OF 27



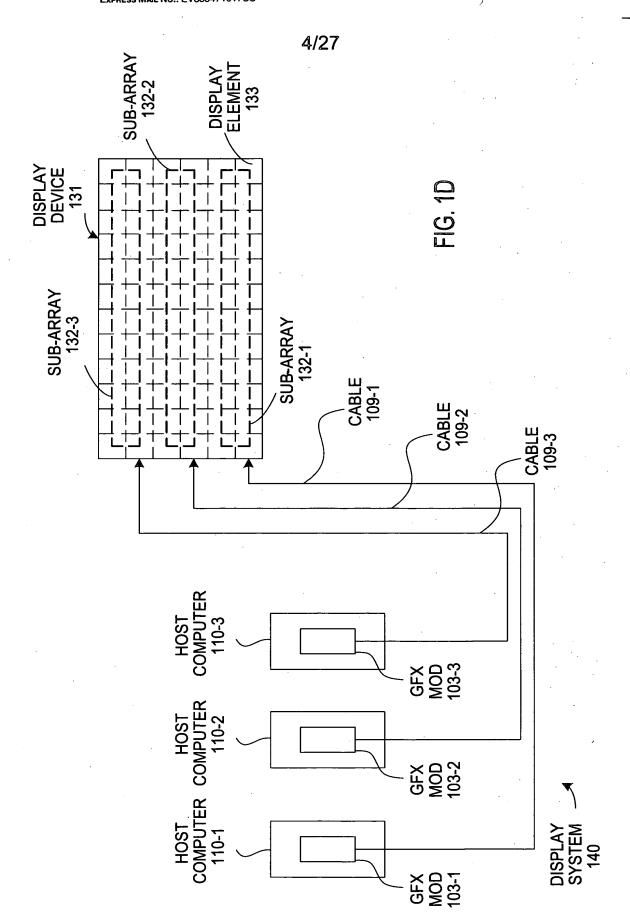
ATTY DKT. No.: U.S. SERIAL NO.: FILED: APPLICANT: TITLE: NVDA/P000876 UNKNOWN HEREWITH

PILED: HEREWITH
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 4 OF 27



ATTY DKT. No.: U.S. SERIAL No.: NVDA/P000876 UNKNOWN FILED: HEREWITH APPLICANT:

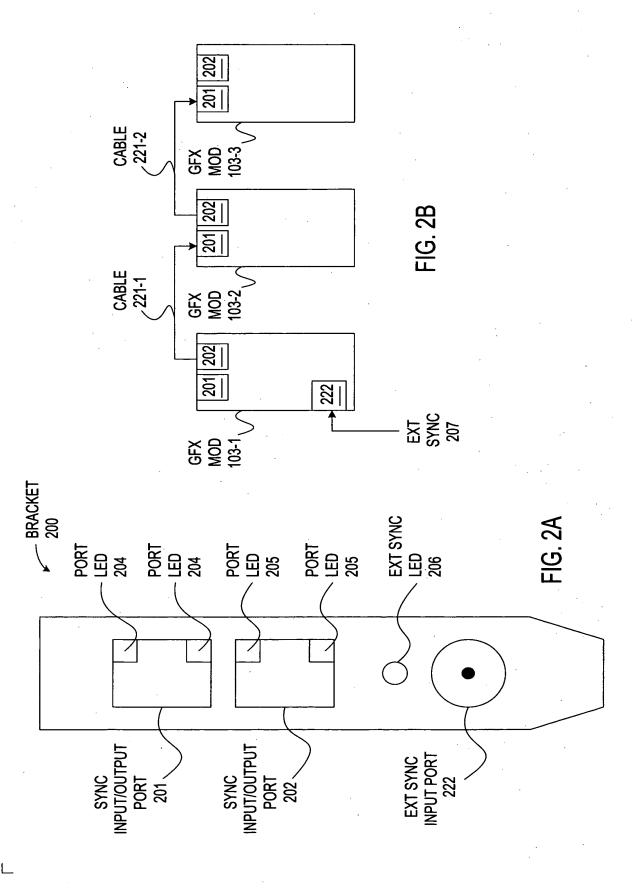
**NVIDIA CORPORATION** METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION TITLE:

STATUS OF A GRAPHICS PROCESSING UNIT MODULE
INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 5 0

PAGE 5 OF 27

5/27



ATTY DKT. NO.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

FILED: HEREWITH
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 6 OF 27

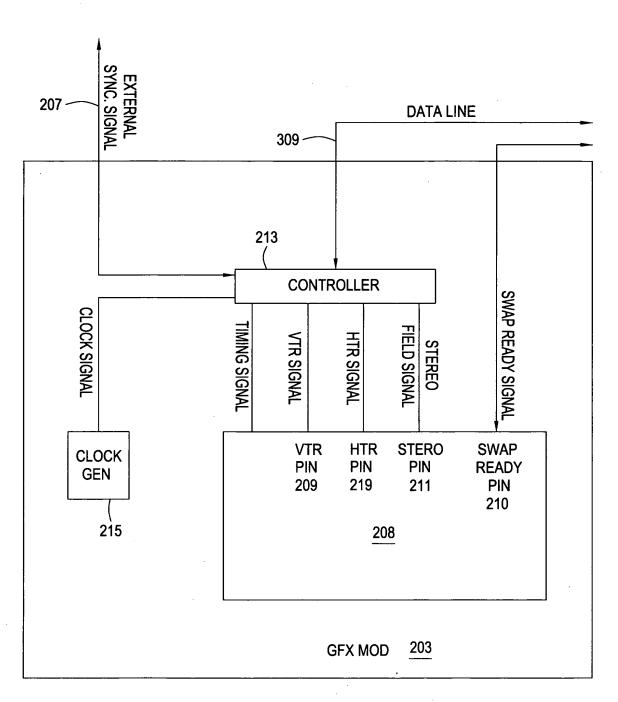


FIG. 2C

ATTY DKT. NO.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

FILED: HEREWITH

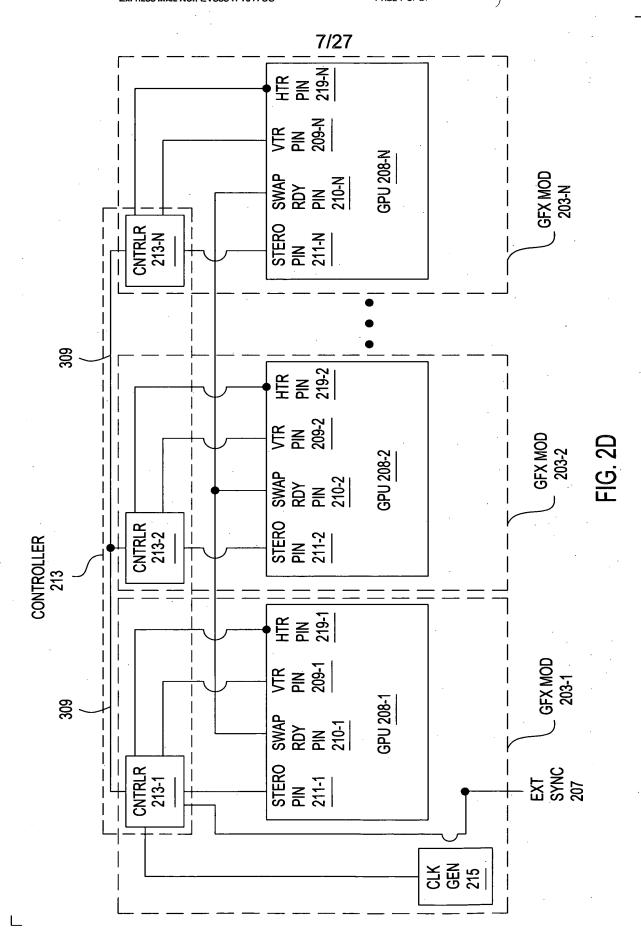
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION

STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 7 OF 27



ATTY DKT. No.: NVDA/P000876
U.S. SERIAL NO.: UNKNOWN
FILED: HEREWITH
APPLICANT: NVIDIA CORPORATION
TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION STATUS OF A GRAPHICS PROCESSING UNIT MODULE
INVENTOR: IAN M. WILLIAMS, ET AL.
EXPRESS MAIL NO.: EV335471017US PAGE 8 OF 27

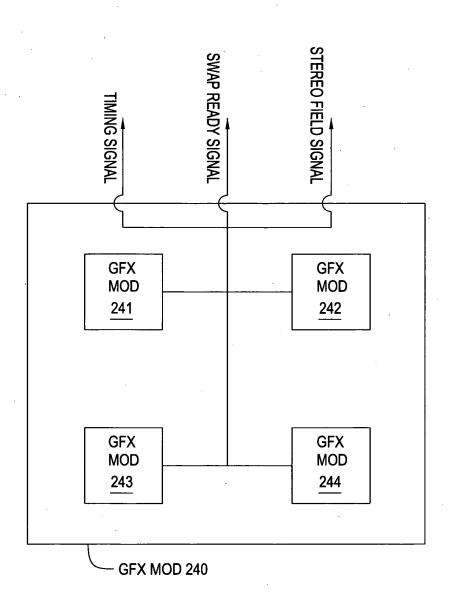


FIG. 2E

NVDA/P000876 UNKNOWN HEREWITH NVIDIA CORPORATION ATTY DKT. No.: U.S. SERIAL NO.: FILED: APPLICANT: TITLE:

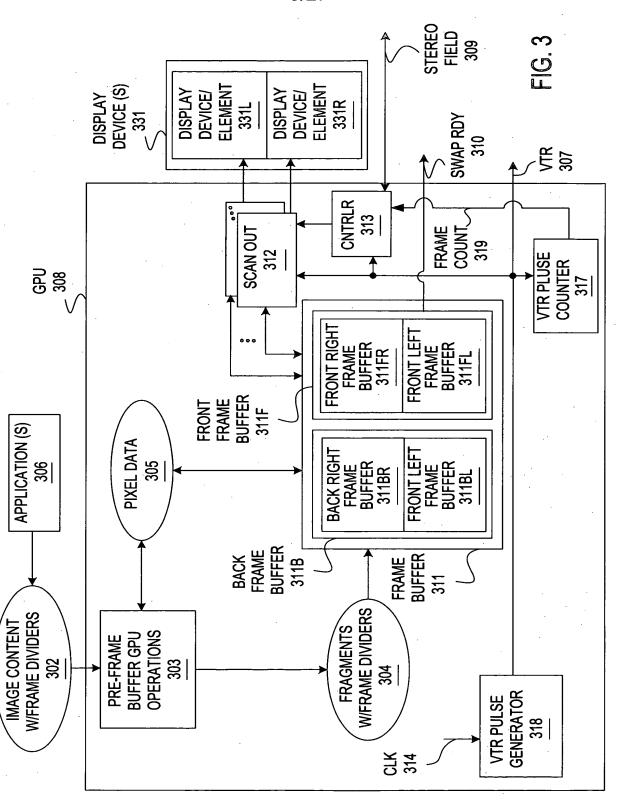
METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION

STATUS OF A GRAPHICS PROCESSING UNIT MODULE INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 9 0



**PAGE 9 OF 27** 



NVDA/P000876
UNKNOWN
HEREWITH
NVIDIA CORPORATION
METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE ATTY DKT. No.: U.S. SERIAL NO.: FILED: APPLICANT:

TITLE:

INVENTOR: IAN M. WILLIAMS, ET AL. EXPRESS MAIL NO.: EV335471017US

PAGE 10 OF 27

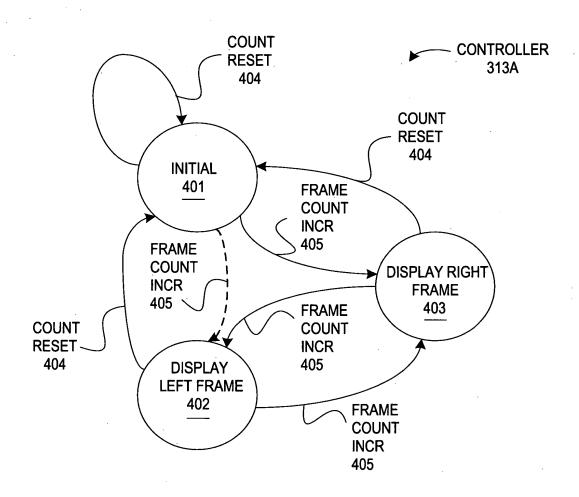


FIG. 4A

ATTY DKT. No.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

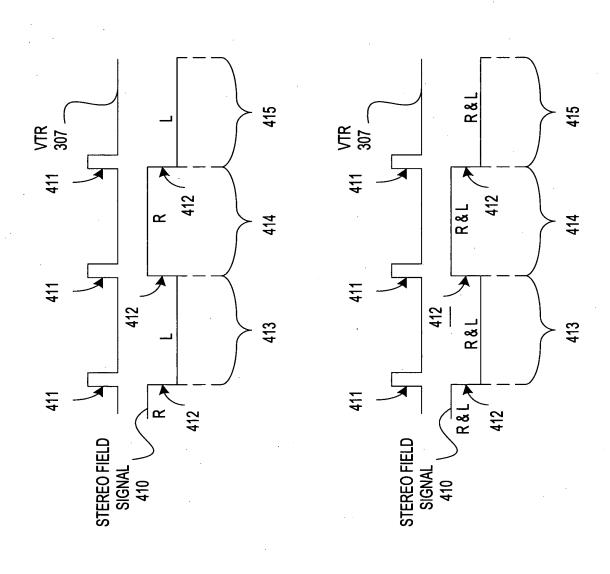
FILED: HEREWITH
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 11 OF 27

11/27



ATTY DKT. No.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

FILED: HEREWITH
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 12 OF 27

 $\lceil \rceil$ 

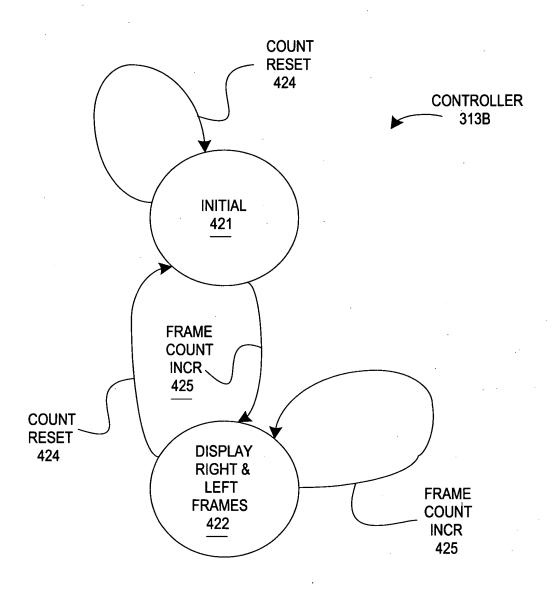


FIG. 4C

ATTY DKT. No.: U.S. SERIAL No.: NVDA/P000876 UNKNOWN FILED: HEREWITH APPLICANT: **NVIDIA CORPORATION** 

STATUS OF A GRAPHICS PROCESSING UNIT MODULE

METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION TITLE:

INVENTOR: IAN M. WILLIAMS, ET AL. EXPRESS MAIL NO.: EV335471017US

PAGE 13 OF 27

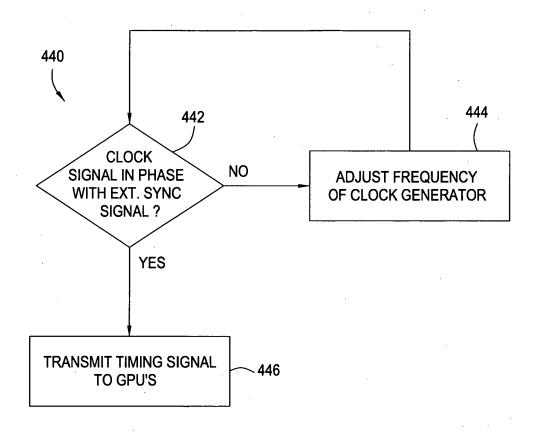


FIG. 4E

NVDA/P000876 ATTY DKT. No.: U.S. SERIAL NO .: UNKNOWN FILED: HEREWITH APPLICANT: **NVIDIA CORPORATION** 

TITLE:

METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION STATUS OF A GRAPHICS PROCESSING UNIT MODULE

IAN M. WILLIAMS, ET AL. INVENTOR: EXPRESS MAIL No.: EV335471017US

PAGE 14 OF 27

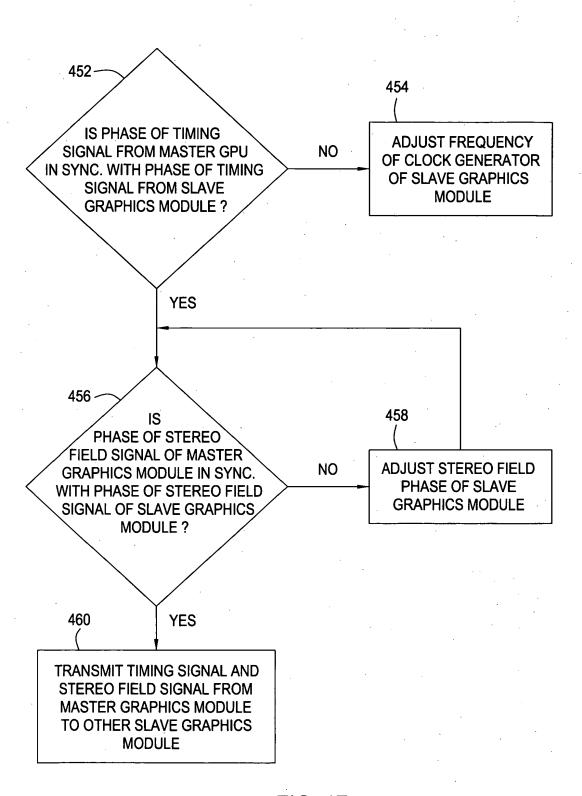


FIG. 4F

ATTY DKT. No.: U.S. SERIAL No.:

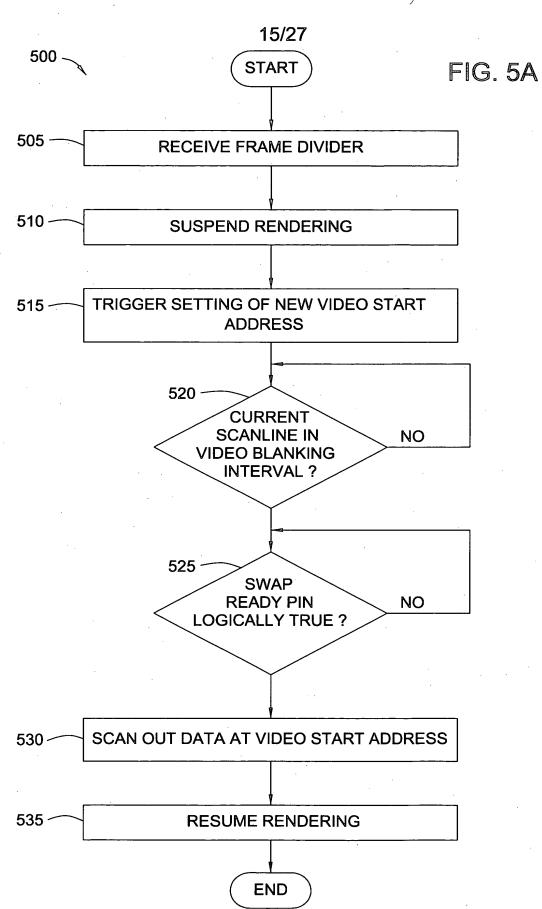
HEREWITH FILED: APPLICANT: **NVIDIA CORPORATION** 

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 15 OF 27

NVDA/P000876 UNKNOWN



NVDA/P000876 UNKNOWN ATTY DKT. No.: U.S. SERIAL No.: HEREWITH FILED: **NVIDIA CORPORATION** 

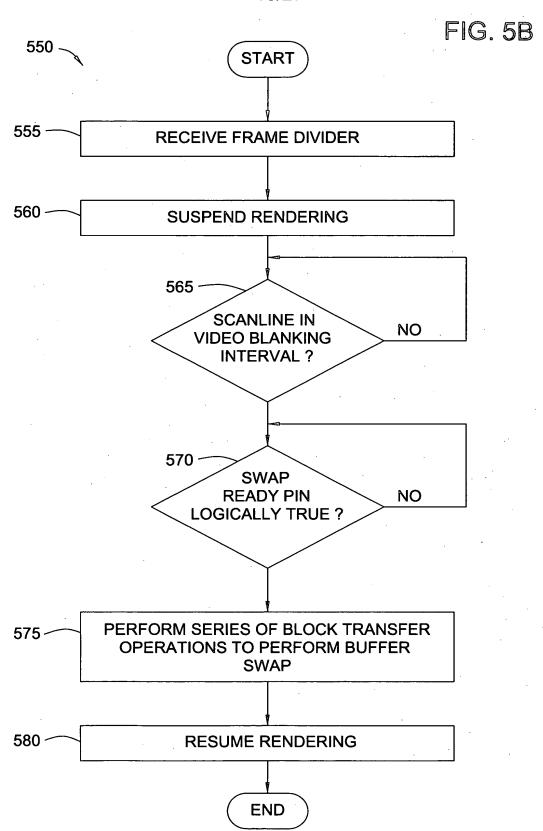
APPLICANT: TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION STATUS OF A GRAPHICS PROCESSING UNIT MODULE

IAN M. WILLIAMS, ET AL.

EXPRESS MAIL No.: EV335471017US

PAGE 16 OF 27





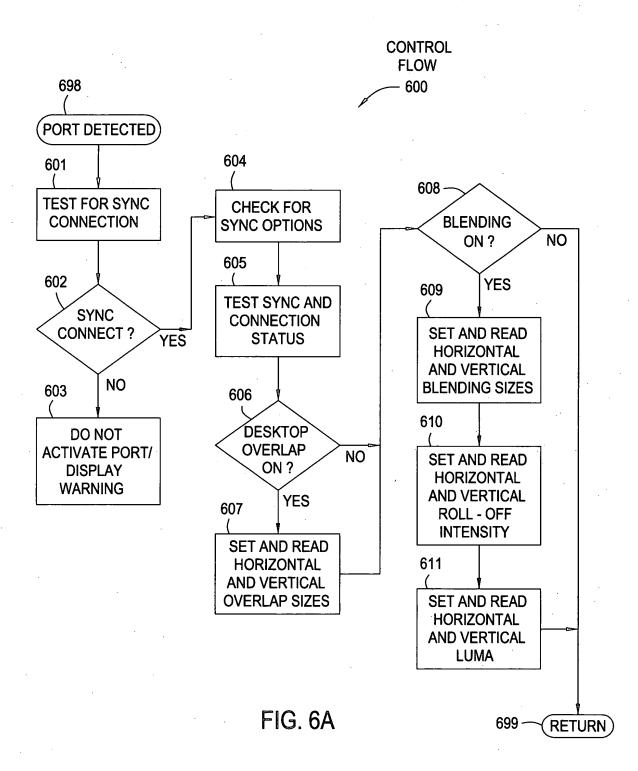
NVDA/P000876 ATTY DKT. No.: U.S. SERIAL NO.: FILED: APPLICANT:

TITLE:

NVIDATIOUS TO UNKNOWN
HEREWITH
NVIDIA CORPORATION
METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL. EXPRESS MAIL No.: EV335471017US

PAGE 17 OF 27



ATTY DKT. No.: U.S. SERIAL No.: NVDA/P000876 UNKNOWN FILED: HEREWITH APPLICANT: **NVIDIA CORPORATION** 

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 18 OF 27

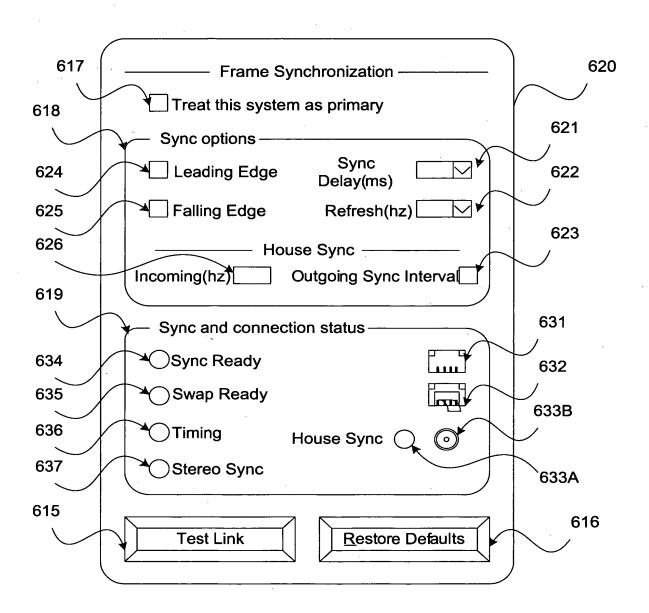


FIG. 6B

ATTY DKT. NO.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

FILED: HEREWITH

APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION

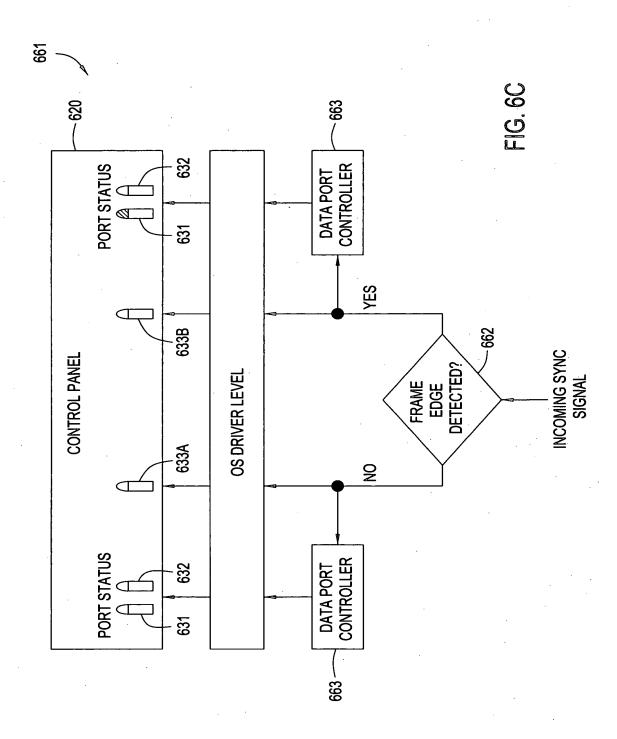
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US

PAGE 19 OF 27

19/27



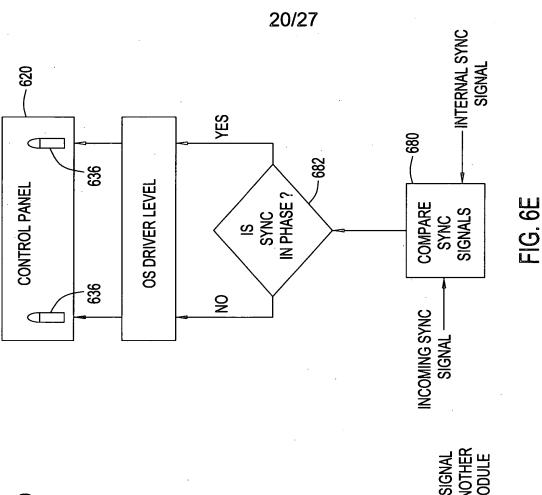
ATTY DKT. No.: U.S. SERIAL NO.: FILED: APPLICANT:

TITLE:

NVDA/P000876
UNKNOWN
HEREWITH
NVIDIA CORPORATION
METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.
EXPRESS MAIL NO.: EV335471017US

PAGE 20 OF 27



STEREO SIGNAL FROM ANOTHER GPU MODULE 620 YES 929 -671 637 OS DRIVER LEVEL CONTROL PANEL IN PHASE ? COMPARE STEREO PHASE 637 2 STEREO SIGNAL FROM GPU MODULE

FIG. 6D

ATTY DKT. NO.: U.S. SERIAL NO.: FILED: APPLICANT:

NVDA/P000876 UNKNOWN HEREWITH NVIDIA CORPORATION

TITLE: METHOD FOR TEST STATUS OF A GRAPI

METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US

PAGE 21 OF 27

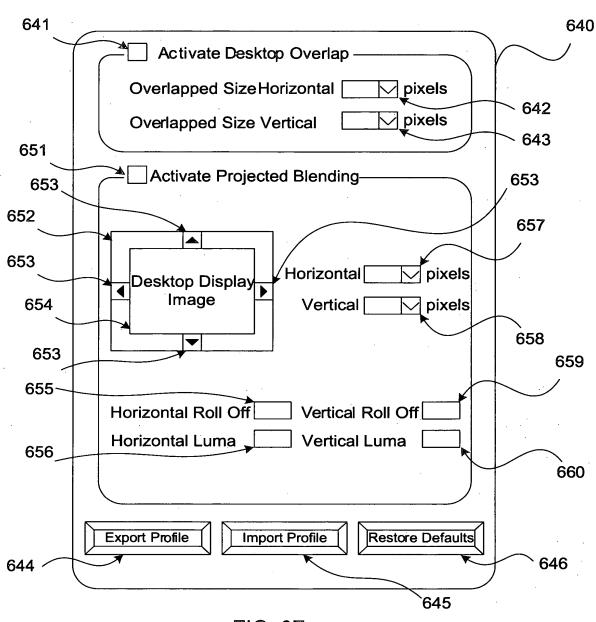
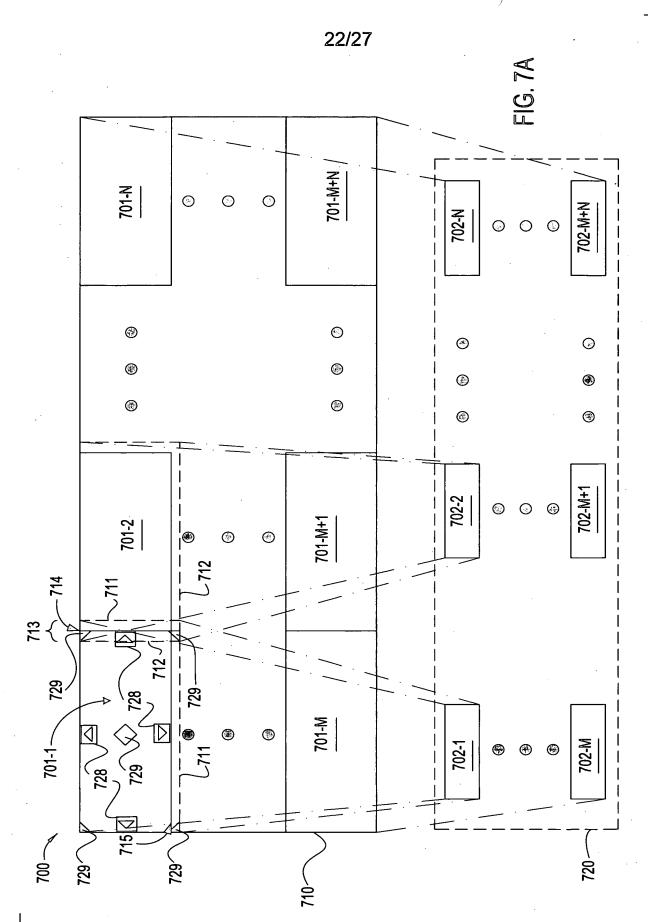


FIG. 6F

NVDA/P000876
UNKNOWN
HEREWITH
NVIDIA CORPORATION
METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE ATTY DKT. No.: U.S. SERIAL NO.: FILED: APPLICANT: TITLE:

INVENTOR: IAN M. WILLIAMS, ET AL. EXPRESS MAIL NO.: EV335471017US

PAGE 22 OF 27



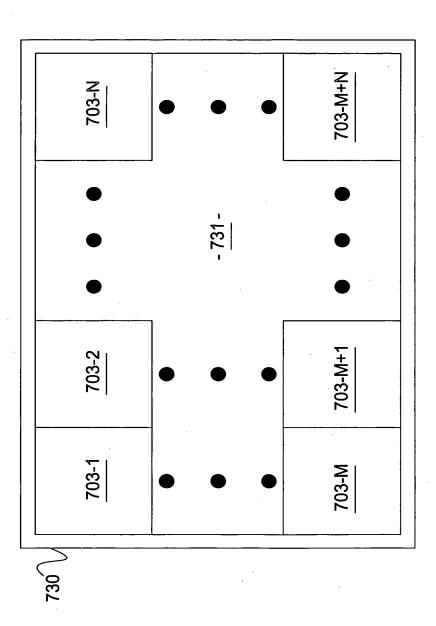
ATTY DKT. NO.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN
FILED: HEREWITH
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 23 OF 27



ATTY DKT. NO.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

FILED: HEREWITH

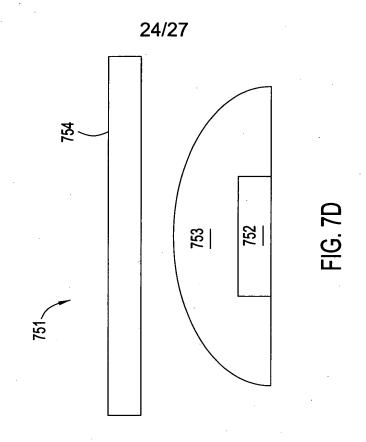
APPLICANT: NVIDIA CORPORATION

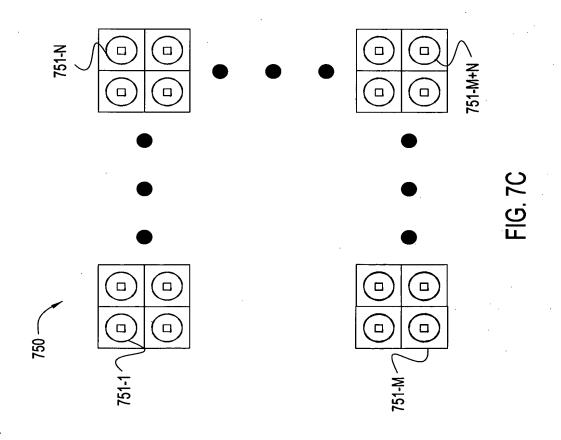
TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION

STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 24 OF 27





ATTY DKT. NO.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

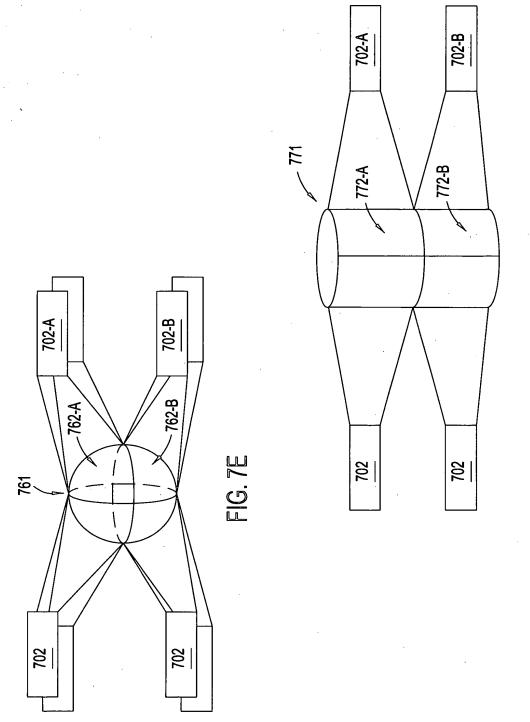
FILED: HEREWITH
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 25 OF 27

25/27



ATTY DKT. NO.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN

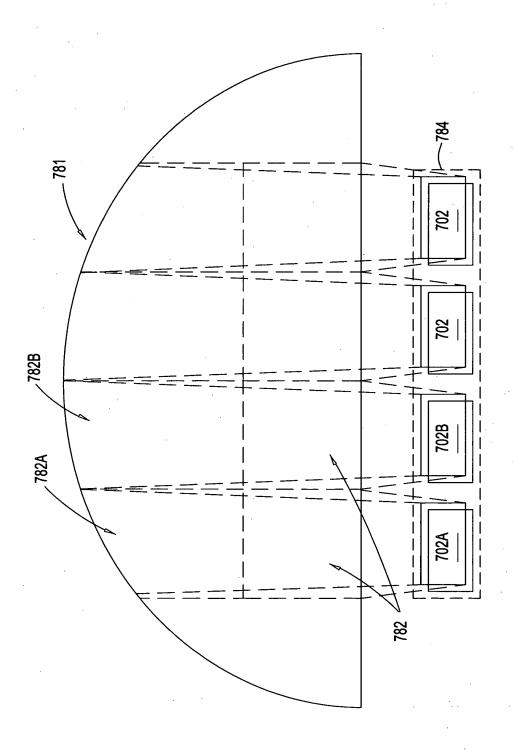
FILED: HEREWITH
APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 26 OF 27

26/27



ATTY DKT. No.: NVDA/P000876

U.S. SERIAL NO.: UNKNOWN
FILED: HEREWITH

APPLICANT: NVIDIA CORPORATION

TITLE: METHOD FOR TESTING SYNCHRONIZATION AND CONNECTION
STATUS OF A GRAPHICS PROCESSING UNIT MODULE

INVENTOR: IAN M. WILLIAMS, ET AL.

EXPRESS MAIL NO.: EV335471017US PAGE 27 OF 27

27/27

